11th International Workshop on Software & Compilers for Embedded Systems

March 13-14, 2008
ICM, Munich, Germany

Heiko Falk

Co-located with DATE 08
About SCOPES 2008

- 22 submissions in total
- from 14 countries
- in 5 continents (Africa, Asia, Europe, N. America, S. America)
- 8 submissions accepted
- Acceptance Rate: 36%

- Highlights: Social event & Keynote talk

- Sponsored by:

- In cooperation with:
Session 1: Optimization and code generation for embedded processors

Session Chair: Hans van Someren

- Integrated Code Generation by Using Fuzzy Control System
  X. Jia, J. Guo, G. Fettweis

- Optimal vs. Heuristic Integrated Code Generation for Clustered VLIW Architectures
  M. V. Eriksson, O. Skoog, C. W. Kessler

- WCET-Driven, Code-Size Critical Procedure Cloning
  P. Lokuciejewski, H. Falk, P. Marwedel, H. Theiling
Session 3: Memory- and addressing-aware compilation

*Session Chair: Koen De Bosschere*

- Communication between Nested Loop Programs via Circular Buffers
  *T. Bijlsma, M. Bekooij, P. Jansen, G. Smit*

- Fast Source-Level Data Assignment to Dual Memory Banks
  *A. Murray, B. Franke*

- A New Heuristic for SOA Problem Based on Effective Tie Break Function
  *H. S. Ali, H. M. El-Boghdadi, S. I. Shaheen*
Session 4: Validation and simulation

Session Chair: Philip Brisk

- A Fully-Non-Transparent Approach To The Code Location Problem
  H. Venturini, F. Riss, J.-C. Fernandez, M. Santana

- Fast Cycle-Approximate Instruction Set Simulation
  B. Franke